

MAIL STOP ISSUE FEE**COMMISSIONER FOR PATENTS****P. O. Box 1450****Alexandria, Virginia
22313-1450****Inventor(s): L. James Hwang et al.****Assignee: XILINX, INC.****Serial No.: 10/082,518****conf.no. 5974****Filed: February 22, 2002****Title: Method and System for Integrating CORES in FPGA-Based System-On-Chip (SoC)****Docket No.: X-1002 US****Enclosed: Return Receipt Postcard
Fee Transmittal - Part B****Date: April 25, 2005****Atty/Sec: KK/pbs****VIA FACSIMILE 703-746-4000**